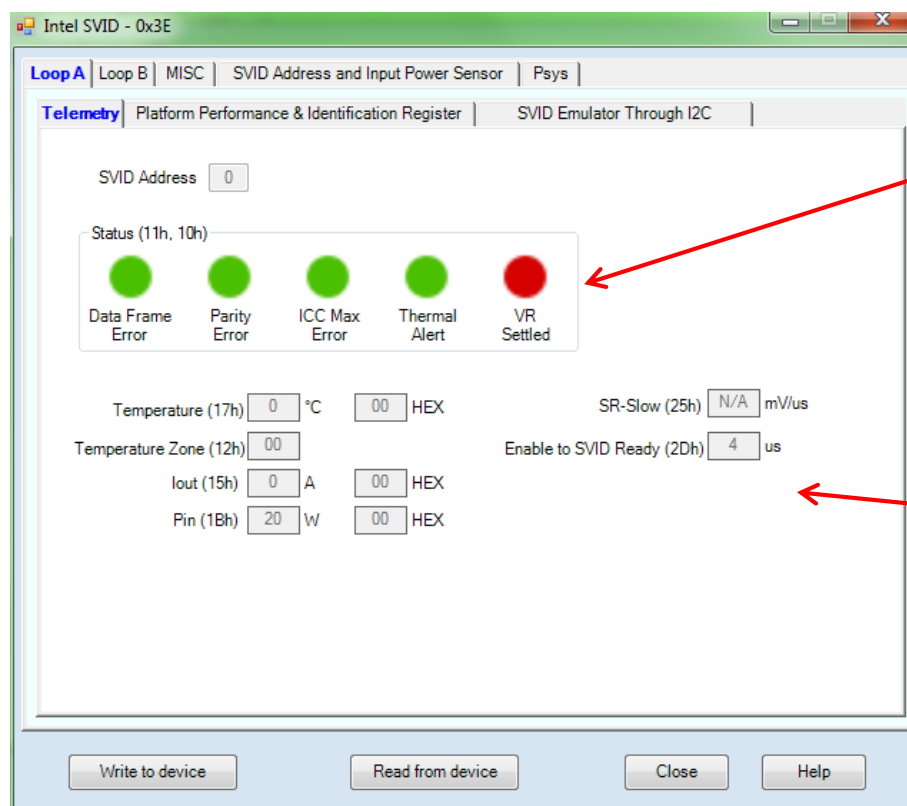


SVID... Telemetry

The Intel SVID window is only available for devices that support SVID



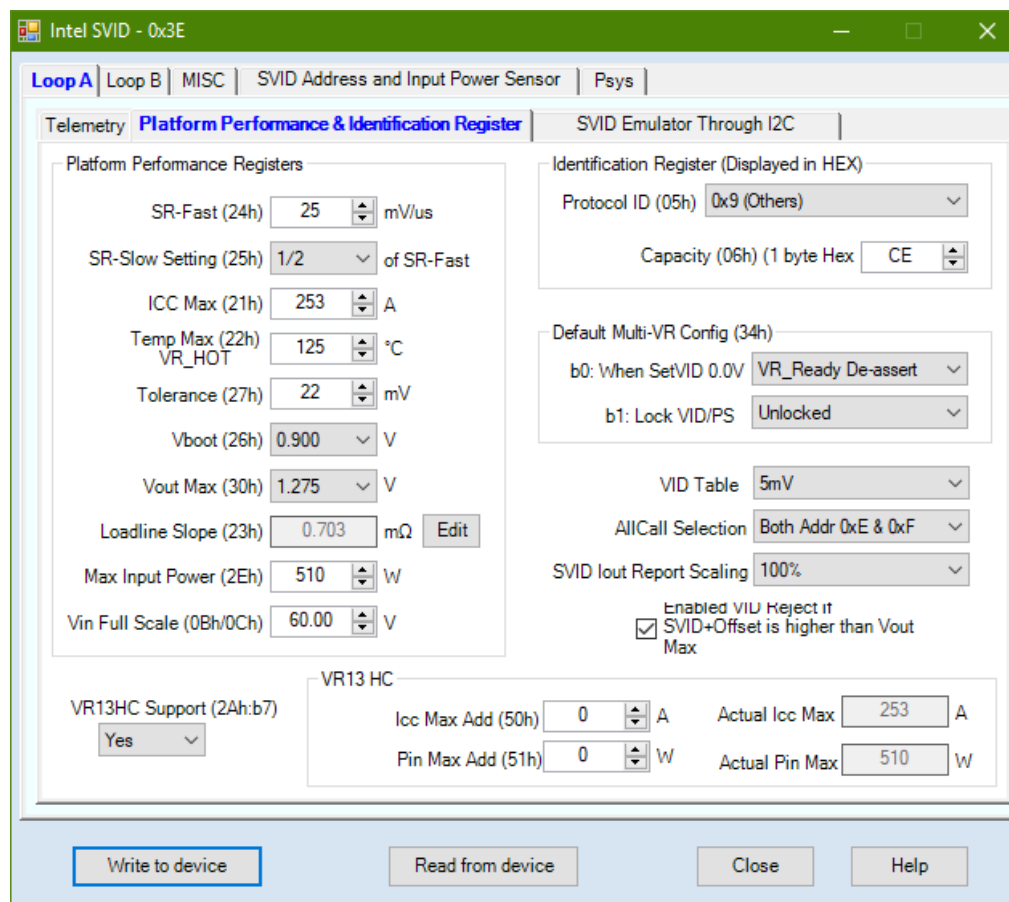
Readout of the status bits in SVID registers.

Readout of parameters in SVID registers.

SVID... Platform Performance

Allow settings of all the registers that SVID use.

Depending on settings some parameters may not be visible if not used i.e. VR13HC settings only visible when VR13HC is activated.



The screenshot shows the 'Intel SVID - 0x3E' application window. The 'Platform Performance & Identification Register' tab is active. The 'Platform Performance Registers' section includes settings for SR-Fast (24h), SR-Slow Setting (25h), ICC Max (21h), Temp Max (22h) VR_HOT, Tolerance (27h), Vboot (26h), Vout Max (30h), Loadline Slope (23h), Max Input Power (2Eh), and Vin Full Scale (0Bh/0Ch). The 'SVID Emulator Through I2C' section includes the Identification Register (Protocol ID 0x9, Capacity CE), Default Multi-VR Config (b0: VR_Ready De-assert, b1: Unlocked), VID Table (5mV), AllCall Selection (Both Addr 0xE & 0xF), and SVID Iout Report Scaling (100%). The 'VR13 HC' section is visible, showing VR13HC Support (Yes) and VR13 HC settings (ICC Max Add 0, Pin Max Add 0, Actual ICC Max 253, Actual Pin Max 510). At the bottom are buttons for 'Write to device', 'Read from device', 'Close', and 'Help'.

SVID... Emulator through I2C

Allow a number of SVID settings to be emulated and sent via I2C to voltage regulator.

Loop B have less number of settings as it is already set in loop A

The screenshot shows the 'Intel SVID - 0x7C' application window. The 'SVID Emulator Through I2C' tab is selected. The interface includes several configuration sections:

- I2C VID:** A text box set to '0.000' with a unit 'V'.
- Slow Slew Rate Selection (2Ah):** A dropdown menu set to '1/2 of SR-Fast'.
- Vout Max (30h):** A text box set to '0.000' with a unit 'V'.
- Power State (32h):** A dropdown menu set to 'PS0'.
- VID Offset (33h):** A text box set to '0' with a unit 'mV'.
- Pin Alert (2Fh):** A text box set to '0' with a unit 'W'.
- Pin Alert Add (52h):** A text box set to '0' with a unit 'W'.
- Actual Pin Alert:** A text box set to '0' with a unit 'W'.
- HC_Active (2Ah : b6):** A dropdown menu set to 'Not Active'.
- Work Point:** A section with five dropdown menus labeled WP0 (3Ah), WP1 (3Bh), WP2 (3Ch), WP3 (3Dh), and WP4 (3Eh).
- Default Multi-VR Config (34h):** A section with two dropdown menus: 'b0: When SetVID = 0V' (set to 'VR_Ready De-assert') and 'b1: Lock VID/PS' (set to 'Unlocked').
- Enabled Write Function:** A checkbox that is checked, with a red arrow pointing to it from the text below.

At the bottom of the window, there are four buttons: 'Write to device', 'Read from device', 'Close', and 'Help'.

Mark the box to enable this I2C SVID emulator function

SVID... Address and Input Power Sensor

Selection of SVID address

SVID address can also be selected using external resistors. Table shows the different combinations of resistor and address

The screenshot shows the 'Intel SVID - 0x3E' application window. The 'SVID Address and Input Power Sensor' tab is selected. On the left, under 'SVID Address', there are dropdowns for 'Loop A Address Base' (0x0), 'Loop B Address Base' (0x1), and 'SVID Address Setting' (based address). On the right, under 'Input Power Sensor SVID Address', there is a dropdown set to 'Normal Rail with SVID Addr 0x0'. Below this is the 'Input Power Sensor Rail' section, which includes a 'Platform Performance & Identification Register' with fields for Max Input Power (2Eh), Max Input Power Add (51h), Actual Input Power, Protocol ID (05h), AICall Selection, and Capability (06h). At the bottom right, the 'SVID Emulator Through I2C' section has an 'Enable' checkbox checked, and fields for Power State (32h), Pin Alert (2Fh), Pin Alert Add (52h), and Actual Pin Alert. At the very bottom, there are buttons for 'Write to device', 'Read from device', 'Close', and 'Help'.

Selection of Input power sensor.

Depending on selections some of the menus may be grayed out as they are not selectable

Allow emulation of SVID via I2C commands

Power state and input power alert **Pin Alert** can be set from GUI when the enable box is marked

SVID... Psys

Intel SVID - 0x3E

Loop A | Loop B | MISC | SVID Address and Input Power Sensor | **Psys**

Psys Source: **Vin x lin** ✓

Input Power Signal Measurement

Gain: 0 Offset: 0.00 W

SVID Registers

Psys Warning 1 Counter (4Eh) 100 1.000 μ s

Psys Warning 2 Counter (4Dh) 100 1.000 μ s

Psys Critical Threshold (4Ah) 0 0 W

Psys Warning 1 Threshold (4Ch) 0 0 W

Psys Warning 2 Threshold (4Bh) 0 0 W

Psys_Critical Assertion De-bounce Time (4Fh) 0 0 μ s

Psys_Critical De-assertion De-bounce Time (49h) 0 0 μ s

☒ Enable write function

Write to device Read from device Close Help

Select which source for Psys calculations

SVID... MISC

This window have SVID settings that are not grouped into other functions.

